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APPLICANTS

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**** CONTINUING DATA *******

This application is a DIV of 09/932,040 08/16/2001 PAT 6,833,291

**** FOREIGN APPLICATIONS *********IF REQUIRED, FOREIGN FILING LICENSE GRANTED****** 10/28/2003**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <i>John P. S.</i> Initials <i>JPS</i>	STATE OR COUNTRY CA	SHEETS DRAWING 6	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 1
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TITLE

Semiconductor processing methods, and semiconductor assemblies

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